

REMARKS

Claims 1, 3, 4, 12, 14, 16, 18, 20 and 22 are presented for consideration, with Claim 1 being independent. Claim 23 has been cancelled without prejudice or disclaimer of the subject matter. No claims have been added.

Applicants amended the specification on page 36 to correct a minor translation error. More specifically, as the Examiner will appreciate, this application was originally filed in Japanese. The English translation was filed in response to a Notice to File Missing Parts of Non-Provisional Application. The above amendment to page 36 is intended to correct a minor error in that translation so that the specification as now set forth with the above amendment is accurate. Acceptance of the above amendment is respectfully sought.

Claims 1, 3, 4, 12, 14, 16, 18, 20 ad 22-23 are rejected under 35 U.S.C. §112, second paragraph, as being indefinite for failing to particularly point out and distinctly claim the subject matter with Applicants regard as the invention. Applicants submit that the above amendments of correcting the translation error in the specification addressed this rejection and that with those amendments the claims do clearly point out and distinctly claim the subject matter which Applicants regard the invention.

The drawings are objected on grounds that they did not allegedly show all the features of Claim 1, and specifically the equal time intervals at the bottom of the graph. This objection is respectfully traversed.

Applicants respectfully submit that the aforementioned features of Claim 1 are show at least in Figures 8 and 9. Figure 8 features abscissa x and ordinate y, wherein a plurality

of normalized luminance which are measured at a plurality of driving time periods, and each of which has equal time intervals less than 5 μ s are plotted on the graph. The plurality of driving time periods do not include $x=0$ and $x=1$, and the number of normalized luminance not falling within a range defined by lines $y=x$ and $y=x^{0.8}$ on the graph is 4/15 or less of the number of the plurality of normalized luminance.

In Figure 8, the number of normalized luminance falling within a range defined by lines $y=x$ and $y=x^{0.8}$ is 6 (at measuring points not including $x=0$ and $x=1$) and the number of normalized luminance points not falling within the range defined by lines $y=x$ and $y=x^{0.8}$ is 1 ($x \approx 0.88$). This number 1 is less than the number 6 (the number of the plurality of normalized luminance points) and $6 \times 4/15$ (1.6). In Figure 9, the number of normalized luminance points falling within a range defined by lines $y=x$ and $y=x^{0.8}$ is 14 (at measuring points not including $x=0$ and $x=1$) and the number of normalized luminance points not falling within the range defined by lines $y=x$ and $y=x^{0.8}$ is 1 ($x \approx 0.8$). This number 1 is less than the number 14 (the number of the plurality of normalized luminance points) and $14 \times 4/15$ (3.733).

Accordingly, Applicants respectfully submit that given the above amendments to the specification and that Figures 8 and 9 to illustrate each and every feature of the invention as set forth in the claims and of the objection to the drawings should be withdrawn.

Claim 1 has been amended to incorporate at least the same features of independent Claim 23. Given those amendments, it is respectfully submitted that Claim 1 is distinguishable over the art of record.

Additionally, Claim 1 is amended as it is respectfully submitted to be supported at least Figures 8 and 9 together with page 35, line 6 through page 36, line 19 of the specification. The plurality of memories in Claim 1 correspond at least to line memories P9 and P10 in Figure 1A and a controller in Claim 1 corresponds to a line memory controller P21 and Figure 1C. The first shift register and the second shift register respectively correspond to shift registers P1101 and P1103 in Figure 1B. In addition, the first memory corresponds to any one or all three line memories P9 in Figure 1A and the second memory corresponds to any one or all three line memories P10 in Figure 1A.

Claim 1 together with the subject matter of Claim 23 incorporated therein was rejected under 35 U.S.C. §103(a) as being unpatentable over Bassetti, Jr. et al. in view of Boudreau et al., Hosokawa et al. and Kishino et al. Due of the above amendments and following remarks, the rejections are respectfully traversed.

Bassetti, Jr. et al. relates to a display system that includes a single shift register 22 but does not teach or suggest the plurality of shift registers as required by Claim 1. Nor is it disclosing a graph whose abscissa x and ordinate y, with a plurality of normalized luminescent points which are measured at a plurality of driving time periods.

The secondary reference to Boudreau et al. features of a graph in which the abscissa x and ordinate y, brightness which is measured at a plurality of driving time periods, the abscissa x represents a pulse width of a driving signal and the ordinate y represents the brightness corresponding to the pulse width. The abscissa x of Boudreau et al., however, is not represented by normalized luminescence normalized to amount of light emitted by the fluorescent substances,

resulting from irradiation by electrons emitted from the electron-emitting devices in the maximum time period. Nor does it teach or suggest a range defined by lines $y=x$ and $y=x^{0.8}$ on its graph.

Kishino is not understood to meet the above shortcomings of the applied references. Kishino features a first memory, a second memory and a third memory, as well as a shift register 61. In Kishino a plurality of signals from the first memory, second memory and third memory are input to the shift register. This is in contrast to the present invention wherein the image forming apparatus comprises a plurality of shift registers including a first shift register and a second shift register, and the first shift register inputs an image signal output from the first memory and the second shift register inputs an image signal output from the first memory. Thus in the present invention, a plurality of signals from a plurality of memories are respectively input to corresponding shift registers and not to a single shift register as disclosed in Kishino.

The foregoing reasons, Applicants respectfully submit that independent Claim 1 is patentable over the applied art of record, whether the art is taken individually or in combination.

The remaining claims of the above application depend from Claim 1 and are therefore patentable over the art of record for reasons noted above with respect to Claim 1. In addition, each recited features of the invention still further distinguishing it from the applied art. Favorable and independent consideration thereof is respectfully sought.

Applicants respectfully submit that all outstanding matters in the above application have been addressed and that this application is in condition for allowance.

Favorable reconsideration and early passage to issue of the above application is respectfully sought.

Applicants' undersigned attorney may be reached in our Washington, D.C. office by telephone at (202) 530-1010. All correspondence should continue to be directed to our below-listed address.

Respectfully submitted,

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